AMP3061P
SOLID STATE HIGH POWER AMPLIFIER

FEATURES

Designed for L-Band High Power Pulse applications
Class AB linear LDMOS design
Built-in protection circuits
High reliability and ruggedness

ELECTRICAL SPECIFICATIONS

Parameter	Specification	Notes
Operating Frequency Range	950 – 1250 MHz	
Output Power Peak Pulse	2 KW Min	Pulse
Power Gain	64 dB Min	
Power Gain Flatness	2.0 dB p-p Max	
Input / Output Return Loss	15 dB / 10 dB Min	
Harmonics	-30 dBc Typ	At rated Pout
Spurious	-60 dBc Max	Non-harmonics
Pulse Width	100 µS	
Pulse Duty Cycle	10 %	
Pulse Droop	1.0 dB	
Rise & Fall Time	75 nS	
Switching Delay	400 nS Typ	
Noise Figure	10 dB Max	
Operating Voltage	50 VDC Nom	
Current Consumption	25 Amp Avg Max	
Max Input Power Protection	+3 dBm Max	<10 Sec without damage
Load VSWR Protection	∞ : 1	Output Isolator

ENVIRONMENTAL CHARACTERISTICS

Parameter	Specification	Notes
Operating Case Temperature	-30 to +60°C	
Storage Temperature	-40 to +70°C	
Relative Humidity	5 to 95 %	Non-condensation

MECHANICAL SPECIFICATIONS

Parameter	Specification	Notes
Dimensions	390 x 224 x 43 mm	See outline drawings
Weight	TBD	
RF Connectors In/Out	SMA-F / Type N-F	
DC Power / Interface Connector	7-Pin Hybrid D-Sub	
Cooling	External Heatsink	Forced air required

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D-SUB CONNECTOR PIN ASSIGNMENT

Pin	Function	Description
1	FWD	Option-101 - Analog Forward Power Indicator
2	VVA	N/C
3	CURRENT SENSOR	I_D@15mV/100mA Typ
4	TEMP SENSOR	V_T@10mV/°C + 500mV Typ
5	SHUTDOWN	TTL "Hi" = Disable Function @ 50mS (**Option**: 5uS Trigger/Pulse Modulator)
A1	VDD	50VDC
A2	GND	Ground

OUTLINE DRAWING